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UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Heon-Deok Park, et al. Confirmation No. 4103  
Serial No.: 10/003,180 Examiner: DeCady, Albert  
Filing Date: October 30, 2001 Group Art Unit: 2133  
Title: SEMICONDUCTOR TEST SYSTEM AND METHOD FOR EFFECTIVELY TESTING A SEMICONDUCTOR DEVICE HAVING MANY PINS

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- ☐ under 37 CFR 1.97 (c) together with either a:  
☐ Statement under 37 CFR 1.97 (e), or  
☐ a \$180 fee under 37 CFR 1.17 (p), or  
(After mailing of first Office Action, but prior to Notice of Allowance or Final Office Action)
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☒ Applicant(s) submit herewith Form PTO 1449- Information Disclosure Citation together with copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

It is requested that the information disclosed herein be made of record in this application.

Any deficiency or overpayment should be charged or credited to deposit account number 13-1703. A duplicate copy of this sheet is enclosed.



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Respectfully submitted,

MARGER JOHNSON &amp; McCOLLOM, P.C.

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